	Application No.	Applicant(s)
Notice of Allowability	09/674,864	BRIGHTMAN ET AL.
	Examiner	Art Unit
	Nabil M. El-Hady	2152
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	ears on the cover sheet with the (OR REMAINS) CLOSED in this or other appropriate communic GHTS. This application is subjection is subjection.	he correspondence address s application. If not included ation will be mailed in due course. THIS
1. This communication is responsive to <u>12/23/2005</u> .		
2. The allowed claim(s) is/are 14-21 and 23, now as 1-9.		
 Acknowledgment is made of a claim for foreign priority unappriority and all blue some color of the: 1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority documents have all copies of the priority documents have all copies of the certified copies of the priority documents have all copies of the certified copies of the priority documents have all copies of the certified copies of the priority documents have all copies of the certified copies of the priority documents have all copies of the priority d	been received. been received in Application N	0
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		eply complying with the requirements
 A SUBSTITUTE OATH OR DECLARATION must be submi INFORMAL PATENT APPLICATION (PTO-152) which give 		
 5. CORRECTED DRAWINGS (as "replacement sheets") muss (a) including changes required by the Notice of Draftsperson 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the deposed of the property of the property of the deposed attached Examiner's comment regarding REQUIREMENT For the property of the pro	on's Patent Drawing Review (F Amendment / Comment or in t 84(c)) should be written on the d ne header according to 37 CFR 1. sit of BIOLOGICAL MATERI	he Office action of rawings in the front (not the back) of 121(d). AL must be submitted. Note the
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/06 Paper No./Mail Date	6. ⊠ Interview Sumn Paper No./Mai 8), 7. ⊠ Examiner's Ame	Date 3/27/2006.
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U.S. Patent and Trademark Office PTOL-37 (Rev. 7-05)

Notice of Allowability

Part of Paper No./Mail Date 20060322

Application/Control Number: 09/674,864

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or

additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR

1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the

payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with

Gordon E. Nelson, Reg. No 30,093 on March 22, 2006.

2. The application has been amended as follows:

In the claims:

I. In Claim 14:

Claim 14 is amended to read:

Claim 14 (currently amended) An integrated circuit comprising:

a plurality of data stream inputs and/or outputs that receive and/or transmit streams of

data;

a plurality of data stream processors that process the streams of data, each data stream

processor is programmable and being coupled to a data stream input and/or data stream output

and each data stream processor processing a stream of data from the data stream input and/or

output, the data stream processor is coupled to as the data stream is received from the data

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stream input and/or transmitted to the data stream output, each data stream processor including:

a writeable instruction memory containing instructions, and a control data processor that controls the data stream processor by sequentially executing instructions from the writeable instruction memory; and the integrated circuit further comprising:

an aggregator to perform receive aggregation to certain of the data stream processors that receive a data stream and to perform transmit aggregation to certain of the data stream processors that transmit a data stream, so that the aggregated data stream processors cooperate in processing a stream of data;

a context processor that responds to information received from a given data stream processor that is processing a data stream to produce information about the given data stream's context and provide the context information to the given data stream processor; the given data stream processor using the context information to process the data stream; and wherein the integrated circuit being constructed on one chip.

II. In Claim 15:

Claim 15 is amended to read:

Claim 15 (currently amended) The integrated circuit set forth in claim 14 wherein: the control data processor is a general-purpose microprocessor that has an industrystandard architecture, whereby programs for the control data processor are developed using standard tools for the architecture.

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New formal drawings in compliance with 37 CFR 1.121(d) are required in this application 3. because drawings submitted with the application is informal. Applicant is advised to employ the services of a competent patent draftsperson outside the Office, as the U.S. Patent and Trademark Office no longer prepares new drawings. The new drawings are required in reply to the Office action to avoid abandonment of the application.

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4. The following is an examiner's statement of reasons for allowance: None of the prior art in record taken alone or in combination discloses an aggregator on the integrated circuit to perform receive aggregation to certain of data stream processors of a plurality of data stream processors that receive a data stream and to perform transmit aggregation to certain of data stream processors of the plurality of data stream processors that transmit a data stream, so that the aggregated data stream processors cooperate in processing a stream of data, and a context processor producing context information to a given data stream processor and using the context information to process the data stream, whereas the integrated circuit that comprises the plurality of data stream processors, the aggregator, as well as the context processor is being constructed on one chip.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nabil M. El-Hady whose telephone number is (571) 272-3963. The examiner can normally be reached on 9:00 - 4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bunjob Jaroenchonwanit can be reached on (571) 272-3913. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Nabil El-Hady, Ph.D., M.B.A. Primary Patent Examiner

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